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PTO/SB/08B (10-01)

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	
		Filing Date	herewith
		First Named Inventor	Alter, M
		Group Art Unit	
		Examiner Name	
Sheet 1 of 1	Attorney Docket Number	M077PI	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
	1	Dr. Luu Nguyen et al, Assembly Considerations for micro SMD Wafer-Level CSPs, Chip Scale Review, May/June 2000, page 48 et seq.	
	2	James L. Young, Wafer-Level Processing: Working Smarter, Chip Scale Review, May 1997, p. 28 et seq.	
	3	Dr. Philip Garrou, Wafer-Level Packaging Has Arrived, Semiconductor International, October 2000, p. 119 et seq.	

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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